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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application:



Rayadurgam Ravikanth, et al

09-30-2002

Serial No: 10/034,020

U.S. Patent & TMOfc/TM Mail Rcpt Dt. #70

Filed: December 28, 2001

For: METHOD OF AND OPERATING ARCHITECTURAL ENHANCEMENT FOR
COMBINING OPTICAL PHOTONIC AND DATA PACKET-BASED
ELECTRICAL SWITCH FABRIC NETWORKS WITH A COMMON SOFTWARE
CONTROL PLANE WHILE PROVIDING INCREASED UTILIZATION OF
SUCH COMBINED NETWORKS.

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

Attention is invited to the prior art identified in the application, the relevance of
which is there discussed.

Respectfully submitted,

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